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APPLICANTS Kiyoshi Kato, Kanagawa, JAPAN; ** CONTINUING DATA ***** This application is a 371 of PCT/JP04/16174 10/25/2004 ** FOREIGN APPLICATIONS ***** JAPAN 2003-373514 10/31/2003 ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/28/2007						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/MAGID Y DIMYAN/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 8	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 3
ADDRESS FISH & RICHARDSON P.C. P.O. BOX 1022 MINNEAPOLIS, MN 55440-1022 UNITED STATES						
TITLE Semiconductor integrated circuit and design method thereof						
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		